

Docket No.: GR 98 P 2895 P

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By: James P. [Signature]

Date: 5/4/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Christian May  
Applic. No. : 09/829,299  
Filed : April 9, 2001  
Title : Method Of Preventing Stack Manipulation Attacks During Function Calls

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 4,545,012 (Clancy et al.), dated October 1, 1985;

U.S. Patent No. 4,701,846 (Ikeda et al.), dated October 20, 1987;

U.S. Patent No. 5,222,220 (Mehta), dated June 22, 1993;

U.S. Patent No. 5,754,762 (Kuo et al.), dated May 19, 1998;

European Application EP 0 540 095 A1 (Bournas), dated May 5, 1993, microcircuit for a chip card having a protected programmable memory;



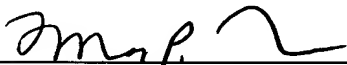
Japanese Patent Abstract JP 62232054 A (Youji), dated October 12, 1987;

Japanese Patent Abstract JP 04084224 A (Hideaki), dated March 17, 1992;

I. Milutinović, Veljko: "High-level language computer architecture" (Advances in VLSI series; 1), ISBN 0-88175-132-4, pp. 156-163;

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

  
For Applicant

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Date: May 4, 2001

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FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE  
STATEMENT BY APPLICANT  
(37 CFR 1.98(b))Attorney Docket No.:  
GR 98 P 2895 PApplic. No.  
09/829,299

Applicant

Christian May

Filing Date  
April 9, 2001

Group Art Unit

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	4,545,012	10/01/85	Clancy			
	B	4,701,846	10/20/87	Ikeda			
	C	5,222,220	06/22/93	Mehta			
	D	5,754,762	05/19/98	Kuo			
	E						
	F						
	G						
	H						
	I						

## FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J	0 540 095 A1	05/05/93	Europe			
	K	62232054 A	10/12/87	Japan			
	L	04084224 A	03/17/92	Japan			
	M						
	N						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	I. Milutinovi'c, Veljkjo; " High-level language computer architecture " (Advances in VLSI series; 1), ISBN 0-88175-132-4, pp. 156-163
	P	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;  
Draw line through citation if not in conformance and not considered. Include copy of this form with  
next communication to applicant.